Filed: 08-05-03 / Serial No.: 10/635,836
For: METHOD AND APPARATUS FOR BORDER NODE BEHAVIOR ON A FULL-DUPLEX BUS

Atty. Docket: APPLE.030DV5/P2834USD4



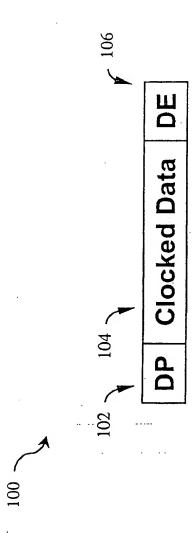


Fig.1 (Prior Art) Alpha Format Packet

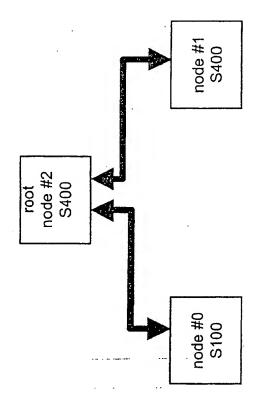
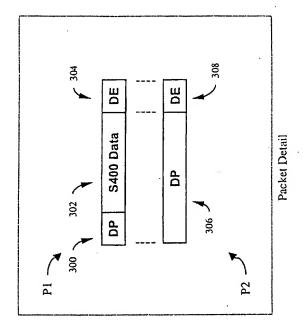


Fig. 2 (Prior Art) Legacy Compatibility Example

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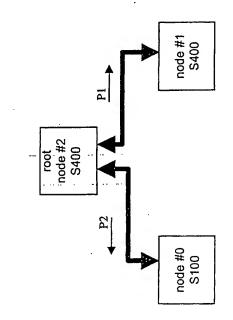


Fig. 3 (Prior Art) Legacy Speed Filtering Example

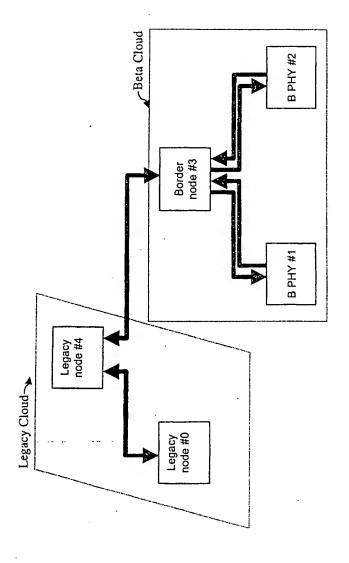
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Fig. 4 (Prior Art) P1394b Packet Format

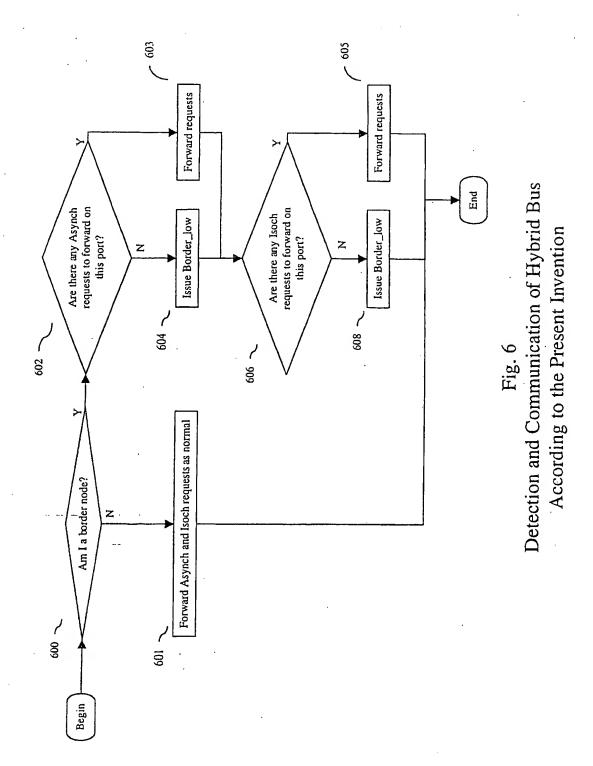


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Data Communications System Having both P1394b and Legacy Devices Fig. 5 (Prior Art)

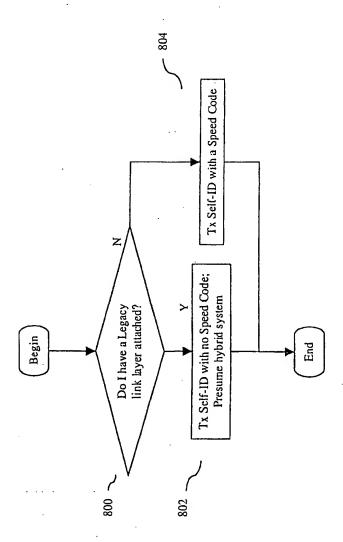


REPLACEMENT SHEET --- Sheet 7 of 13

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Atty. Docket: APPLE.030DV5/P2834USD4

REPLACEMENT SHEET --- Sheet 8 of 13



Detection and Communication of a Legacy Link According to the Present Invention Fig. 8

According to the Present Invention

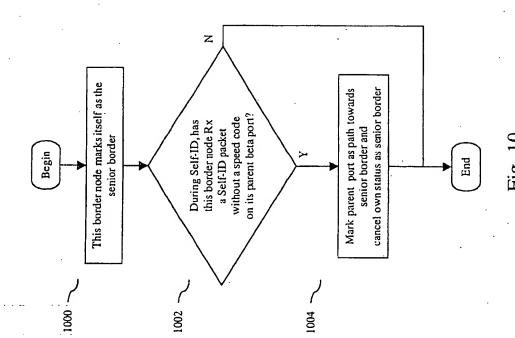
Presume hybrid system

Presume hybrid system

Big. 9

Betection and Communication of Hybrid Bus

REPLACEMENT SHEET --- Sheet 10 of 13



According to the Present Invention Detection of Path to Senior Border By a Border PHY Fig. 10

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REPLACEMENT SHEET --- Sheet 11 of 13

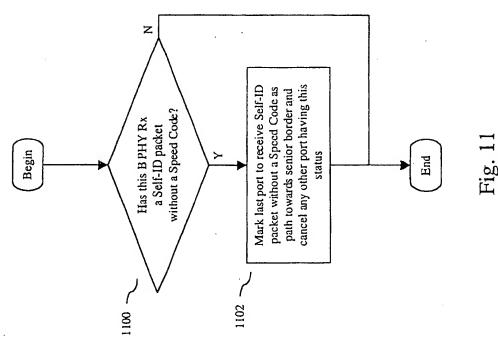
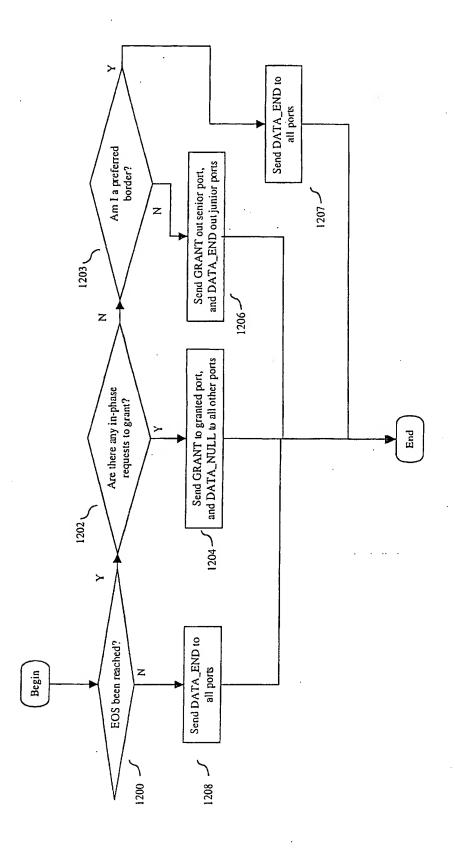


Fig. 11 Identification of Senior Border Node According to the Present Invention By a B PHŸ

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According to the Present Invention Restoring Senior Border as BOSS Fig. 12

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Hybrid Bus ARG ARG ARG SAG SAG SAG LOP EOP EOP EOP ACK Missing B Bus EOP EOP EOS (After asynchronous packet) !EOS (After asynchronous packet) End of Isoch Packet

According to the Present Invention Fig. 13 OK_TO_GRANT Profile